

IN THE SPECIFICATION:

On page 39, lines 8-20, replace the entire paragraph with the following paragraph as amended:

A1
5 The mode selectable circuit further includes transistors 1609, 1611 and 1613, each
having a gate terminal coupled to receive the mode select signal 1102. A drain terminal of
transistor 1609 is coupled to the source terminals of transistors 606 and 608, and a source
terminal of transistor 1609 is coupled to receive an input GT signal (e.g., GT_{i-1}). A drain
terminal of transistor 1611 is coupled to a gate terminal of transistor 1121, and a source
terminal of transistor 1611 is coupled to receive a complemented comparand signal from a
10 comparand signal line. A drain terminal of transistor 1613 is coupled to the match line 127,
and a source terminal of transistor 1613 is coupled to the drain terminals of transistors 1121
and 1125. In the embodiment of Fig. 16, transistors 1613 and ~~1611~~ ~~1515~~ are PMOS
(Positively doped, Metal Oxide Semiconductor) devices that are switched on (i.e., enter a
conducting state) when the mode select signal 1102 is low, and switched off when the mode
15 select signal 1102 is high. Transistor 1609 is an NMOS (Negatively doped, Metal Oxide
Semiconductor) device that is switched on when the mode select signal 1102 is high, and
switched off when the mode select signal is low.